800.0039



IN THE UNITED STATE IT AND TRADEMARK OFFICE

Applicants:

Pechanek et al.

Serial No.:

09/422,015

Filed:

October 21, 1999

For:

METHODS AND APPARATUS FOR ABBREVIATED INSTRUCTION SETS ADAPTABLE TO CONFIGURABLE

PROCESSOR ARCHITECTURES

Group:

2183

Examiner:

Kim, K.

CERTIFICATE OF MAILING (37 CFR 1.8a)

1 hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231, on the date set forth below:

Signed:

Date: November 16, 2001

Chapel Hill, North Carolina November 16, 2001

Commissioner for Patents **BOX NON-FEE AMENDMENT** Washington, DC 20231

Amendment

Sir:

JAN 1 8 2002 Technology Center 2100 Please amend the above identified application as follows:

## In the Drawings

Red-lined proposed drawing corrections to sheet 1 of the drawings are submitted herewith for the Examiner's approval.

## In the Specification

Please replace the paragraph beginning at page 3, line 14, with the following rewritten paragraph: